

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

|                   |  |   |  |
|-------------------|--|---|--|
| Applicant(s) :    | LAKSHMANAN,<br>Viswanathan et al.  | ) | <u>CERTIFICATE OF TRANSMISSION/MAILING</u>   |
| Serial No.:       | 10/828, 408  | ) | I hereby certify that this correspondence is being<br>facsimile transmitted to the USPTO or deposited with<br>the United States Postal Service with sufficient postage<br>as first class mail in an envelope addressed to:<br>Commissioner for Patents, P.O. Box 1450, Alexandria,<br>VA 22313-1450 on the date below. |
| Confirmation No.: | 4517   | ) |  |
| Filed:            | April 19, 2004   | ) | <u>September 1, 2006/ Eric James Whitesell /</u><br>Eric J. Whitesell #38657   |
| For:              | METHOD AND COMPUTER<br>PROGRAM FOR VERIFYING AN<br>INCREMENTAL CHANGE TO AN<br>INTEGRATED CIRCUIT DESIGN | ) |  |
| Art Unit:         | 2825   | ) |  |
| Examiner:         | Parihar, Suchin  | ) |  |

**AMENDMENT "B" UNDER 37 C.F.R. § 1.116**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Please enter the following response to the final  
Office Action mailed on August 15, 2006.

Status of the Claims            pp. 2-5  
Remarks                         pp. 6-10